



# IMPROVED PERFORMANCE OF PHOTOVOLTAIC BASED EMBEDDED DUAL POWER SOURCE SL-QUASI Z SOURCE INVERTER FOR IM DRIVE

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## ABSTRACT

This paper presents the performance of PV-based embedded dual power source of SL quasi Z source inverter. The SL quasi Z source inverter with an embedded PV source and a dual power source (DPS) is injected into the system. The DPS helps the system to operate at an equilibrium state and stabilizes the entire system during PV power fluctuations. The design of the proposed system is carried out in MATLAB/Simulink environment. The control strategy with the PI controller is adopted. ESLQ Z source inverter with voltage boost method is employed with basic SPWM methodology will be suitable for IM drive applications. The system has the merit of a low inrush current that will protect and increase the life of switching devices. Finally, the study and simulation setup is validated by proving their good operating features. The proposed work is trustworthy that minimizes the total distortion factor, start-up inrush current with improved performance.

**KEYWORDS:** Photo voltaic (PV), embedded dual power SL quasi Z source inverter, Induction motor drive (IM), PI controller.

## 1. INTRODUCTION

The decreasing of fossil fuels and increasing environmental pollution leads the researcher to concentrate on ever-green technology such as renewable energy. In such cases, the researcher has to adopt a novel and robust system to ensure the system's capability and performance metrics. Meanwhile, a lot of parameters influence the complexity of the entire system that will affect the performance. The proposed work will eliminate the complexity and increases the performance of the embedded dual power SL quasi Z source inverter with the simple modulation technique, rapid boost voltage, and robust structure. Impedance source inverters can be utilized for both buck and boost functions in a unique system.

Turning ON the same phase leg can be made simultaneously is possible in this type of setup and not available in conventional voltage source inverter (VSI) and current source inverter (CSI) [1]. Control strategy for quasi z source inverter with battery and PV system employs a modified SVM technique to minimize the harmonics content in the output voltage [2]. Proposed PS-PWAM for PV QZS network with MLI with a reduced number of switches leads to reduce the power loss in [3].

A small-signal dynamic model for QZSI is proposed with the combination of battery bank and SVM control strategy to achieve the low inductor current and higher efficiency [4]. The voltage-fed quasi z source inverter topology is presented in [5] is suitable for light and heavy loads that are implanted with a DSP processor. It eliminates the problems of abnormal work state in light loads. Voltage stress, inrush current, and common ground problems are avoided in [6]. For low power applications, switched boost inverter (SBI) is replaced by a Z source inverter that reduces the LC pairs [7]. To overcome the drawback in conventional Z source inverter, a new high voltage boost impedance source inverter i.e. switched coupled inductor Quasi Z Source inverter has been proposed with the integration of switched capacitor and switched inductor [8]. Double frequency power mismatch will affect the dc input and ac output, this can be avoided with the help of the capacitance reduction control strategy is proposed [9]. Quasi Z Source inverter in boost mode and grid-tie current controller with plug-in repetitive control is developed in [10]. The QSLZSI is the combination of traditional ZSI and CHB circuits that reduces the count of the components and achieves high boost factors [11]. A new control strategy to suppress the resonance on the impedance networks is proposed in [12]

The proposed work focuses on an SL quasi Z source inverter with an embedded PV source and a dual power source. This topology employs the dual battery power source to gain the voltage level during fluctuations and to maintain an equilibrium operating state. The simple SPWM technique is used to reduce the complexity in the existing traditional SL Quasi Z Source inverter. The PV fluctuations by PV panels are eliminated in this system and it is suitable for IM drive applications.

## 2. PROPOSED EMBEDDED DUAL POWER SOURCE SL-QUASI Z SOURCE INVERTER

Figure 1 shows the traditional single-phase QZSI for PV power conversion. The QZSI has an impedance network consisting of a couple of inductor  $L_1$  &  $L_2$ , a couple of capacitor  $C_1$  &  $C_2$ , and a diode  $D_1$  in the photovoltaic section with a dc link.

Where,  $C_p$  is the PV panel capacitor,  $V_o$  is the ac output voltage and  $L_f$  is the filter inductor. Meanwhile,  $V_g$  and  $i_s$  denotes grid voltage and grid-tie current.

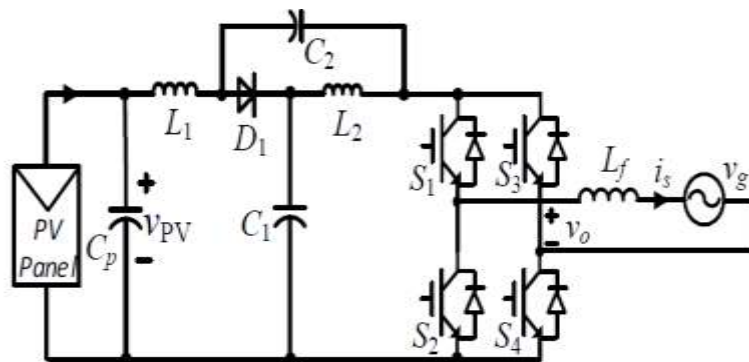
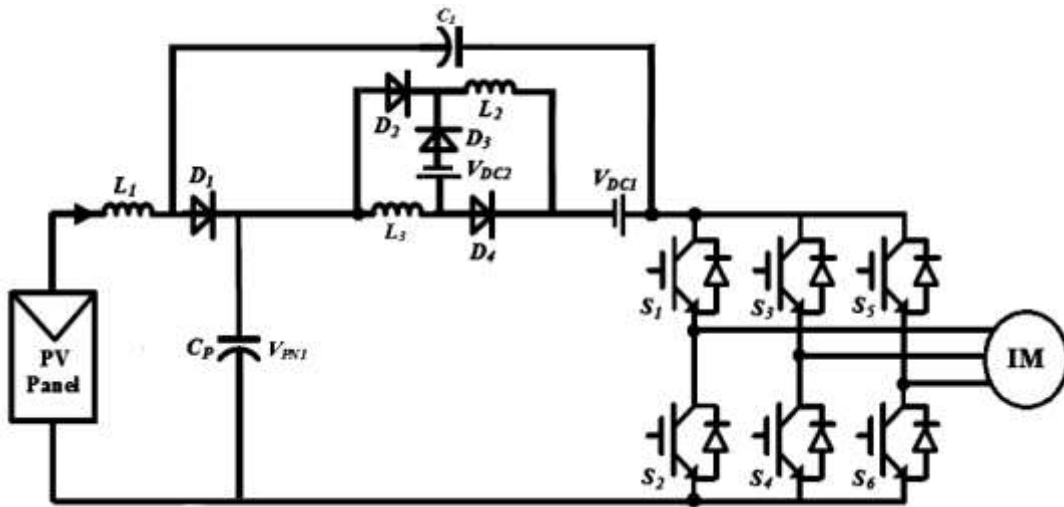


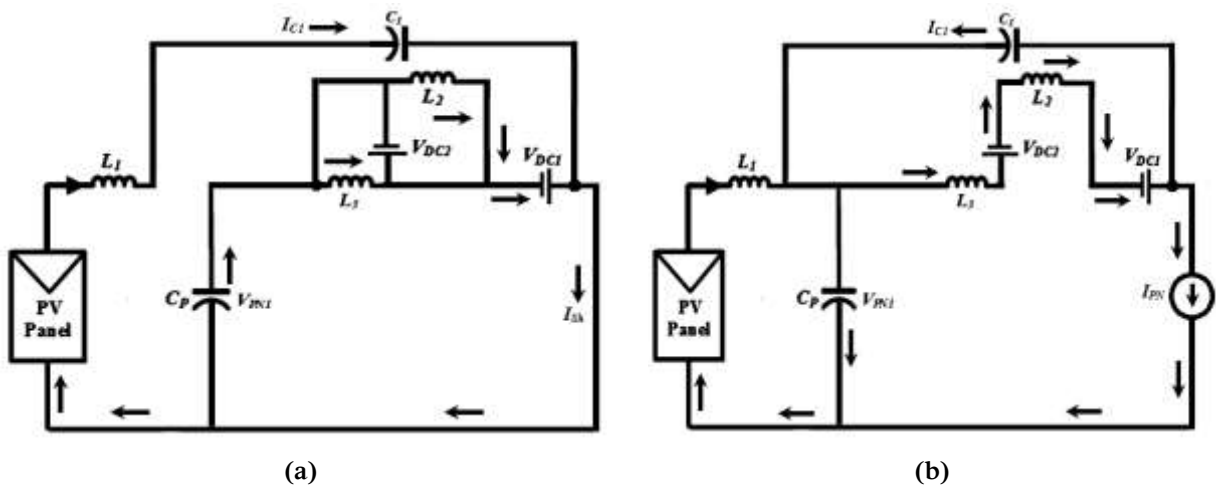
Figure 1. Traditional SL quasi Z source inverter

To avoid PV fluctuations, one or dual dc sources can be connected to an impedance network will make a smoother input current flow and avoids fluctuations. This topology can boost the voltage gain. Figure 2 shows the SL Quasi Z Source inverter with an embedded dual power source for IM drives consisting of battery  $V_{DC1}$  and  $V_{DC2}$  are connected in series to the diode  $D_3$ . The scope of this proposed configuration has to overcome continuous fluctuation input current and also suppress the start-up inrush current caused by the leakage inductance. It can be absorbed by the capacitor  $C_1$  and recycled without creating any voltage spike in the target.



**Figure 2: Proposed SL quasi Z source inverter with embedded dual power source for IM drives**

Figure 3(a) shows the equivalent circuit in the shoot-through state of the proposed inverter and figure 3(b) shows the equivalent circuit in the non-shoot through state. Both equivalent circuits, 3 (a) and (b) add two capacitors, three inductors, and a dual voltage source.



**Figure 3: a) Equivalent circuit in shoot-through state. b) Equivalent circuit in non-shoot through state**

This topology is built with a battery and PI controller to have closed-loop operations. The PI controller has robust system performance and reduced fluctuations.

The output voltage  $V_0 = V_0 \sin \omega t$ , where  $\omega$  is the amplitude and fundamental frequency of ESLQZSI output voltage. During the SPWM period, the dc-link peak voltage  $V_{DC} = V_{PV}$ . Hence the voltage fluctuations are eliminated. The modulation index  $M$ , angular position  $\theta$ , shoot-through duty cycle  $D(t)$ , and the peak voltage  $V_{dc}$  of the ESLQZSI can be derived and as follows,

$$M_a = \frac{V_0}{V_{pv}} \tag{1}$$

$$\theta = \sin^{-1} \frac{1}{M} \tag{2}$$

$$D_i(\omega t) = \frac{1 - |M \sin(\omega t)|}{1 - 2|M \sin(\omega t)|}, V_{DC} = \frac{1}{1 - 2D_i(\omega t)} V_{pv} \quad (3)$$

### 3. PROPOSED CONTROL STRATEGY OF SPWM WITH PI CONTROLLER

Figure 4 shows the control strategy of the proposed ESLQZSI with the SPWM technique for a single leg along with an MPPT controller to utilize the full harvested power from the PV panel. The following control objectives are implemented in the embedded dual power SLQZSI. 1. Adaptation of MPPT algorithm. 2. Constant dual power source to avoid PV power fluctuations and, 3. Battery handling system. The PI regulator is implemented to verify the proposed topology. The harvested PV voltage and its reference  $V_{PV}$  from MPPT algorithm, and current  $I_{PV}$  are obtained from the controller to form the phase angle. Feedback of induction motor torque  $T_m$  is given to the PLL. The dual power source with closed-loop control will achieve the required shoot-through duty ratio to handle the dual battery system to operate in real-time. To achieve the equilibrium condition with fast response, the feed-forward shoot-through duty ratio is given as

$$D = \frac{V_b}{(V_{pv} + 2V_b)} \quad (4)$$

Where  $V_{pv}$  is the reference output voltage obtained from MPPT, the dual battery power is derived as,

$$2P_b = P_{out} - P_{pv} \quad (5)$$

Where  $P_{out}$  is the grid injected power to eliminate power fluctuations

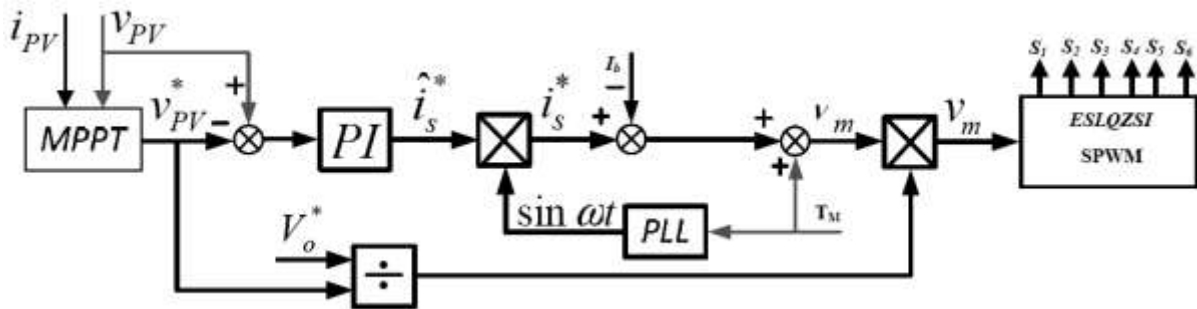


Figure 4. Control strategy of SPWM modulated single leg ESLQZSI for IM drives

The dual power source open loop transfer function is given as,

$$D_{ual}(s) = \frac{i_b(s)}{d_s(s)} \quad (6)$$

Closed loop transfer function is given by,

$$D_{ual}(s) = \frac{d(s)}{i_b^*(s)} \quad (7)$$

From battery power reference, we can get the dual power source reference.

The PI controller coefficient is given by

$$P = \frac{1}{V_{pv}} \quad (8)$$



#### 4. SIMULATIONS AND EXPERIMENTAL VERIFICATION

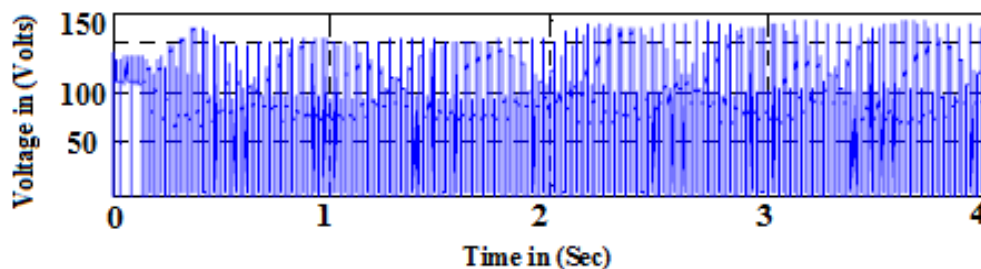
To validate the proposed system, the simulation is carried out in MATLAB/Simulink environment for ESLQZSI with a PI control strategy. Figure 5 presents the harvested output PV voltage of 150volts and is fed to MPPT with PI controller. Figure 6 describes the PV current characteristics and varies with the illumination level of light rays. Figure 7 shows the three-phase voltage waveform of the ESLQZSI fed IM drive without any fluctuation in voltage. Figure 8 gives the output current waveform. Figure 9 demonstrates the IM speed characteristics and rated speed of 1500RPM is achieved. Figure 10 gives information about the torque characteristics. Figure 11 shows THD values at the fundamental frequency of 50Hz, with a voltage level of 234.4, and achieves the total harmonics distortion of 1.60%. These results prove that the system is operating in stable conditions.

**Table 1 shows the parameter used for the simulation and experimental**

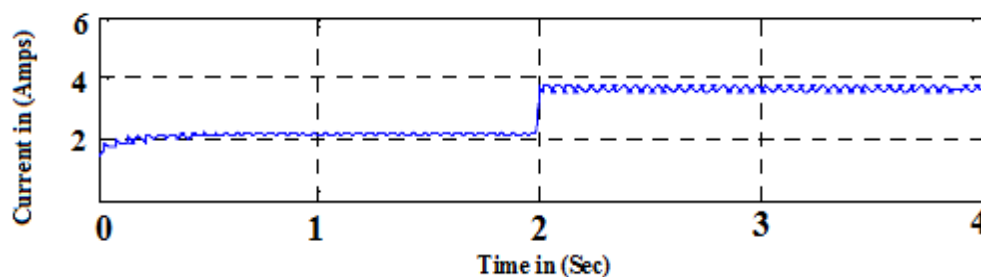
Sl. No.	Parameters	Values
1	Open circuit voltage of PV system	150V
2	Short circuit current	8.7A
3	Maximum power voltage	175V
4	Maximum power current	7.94A
5	Inductor	5 Henry
6	Capacitor	2800 $\mu$ F
7	MOSFET	25N120
8	Control strategy	PI controller

**Table 2 PV panel specifications**

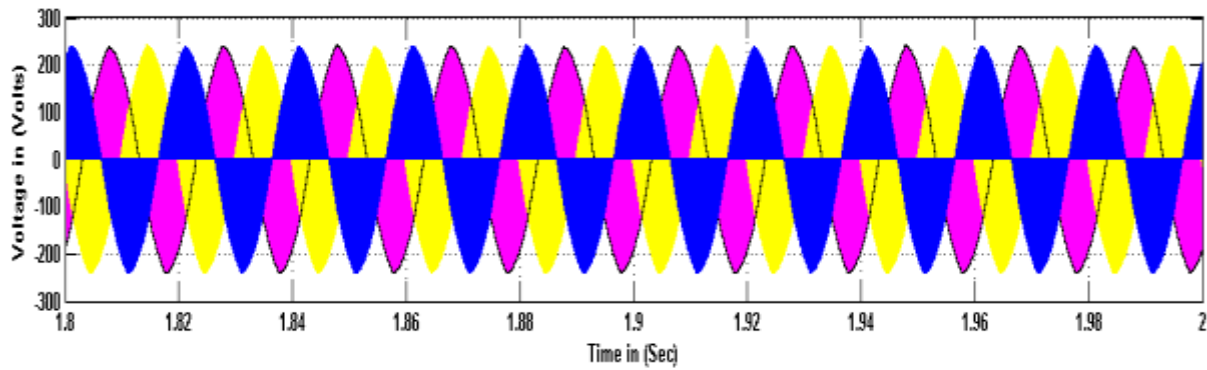
S.No	Specification	Value
1	Maximum Power (Pmax)	300 Watts
2	MPP voltage	32.0V
3	Open circuit voltage (Voc)	39.8V
4	MPP current	9.40Amp
5	Short circuit current	9.98Amp



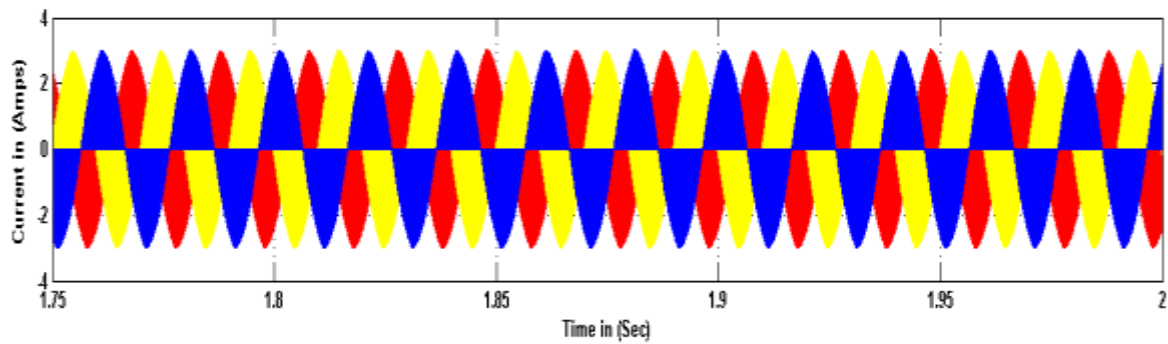
**Figure 5: Harvested PV voltage with fluctuation**



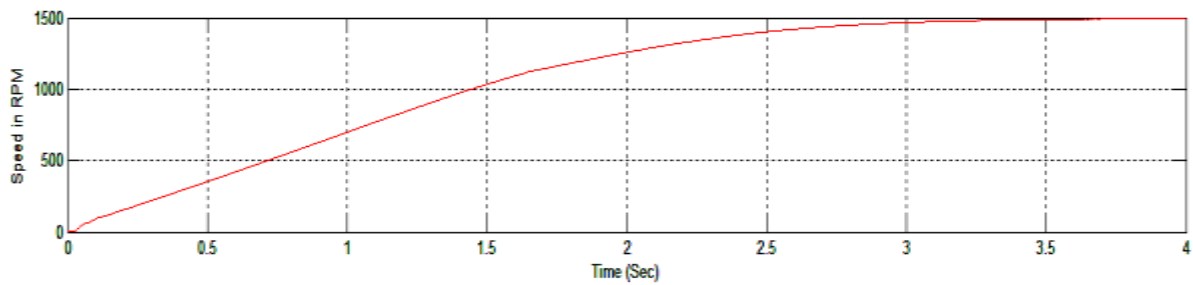
**Figure 6: Harvested PV current with fluctuation**



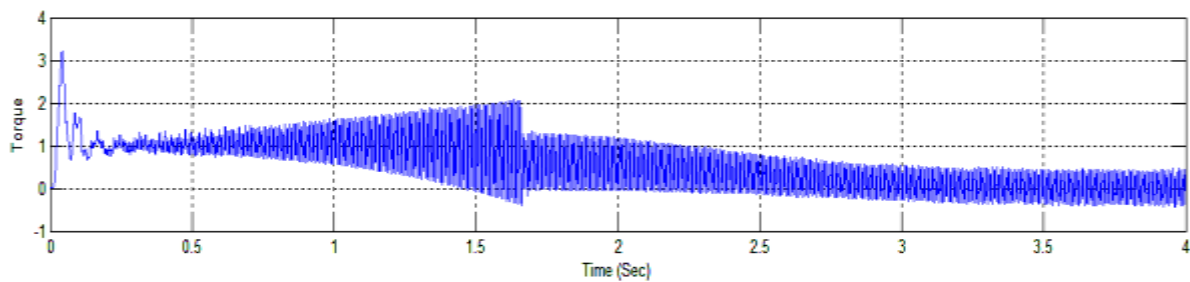
**Figure 7: Three phase voltage waveform of ESLQZSI without fluctuation**



**Figure 8: Three phase current waveform of ESLQZSI without fluctuation**



**Figure 9: Induction motor speed characteristics**



**Figure 10: Induction motor torque characteristics**

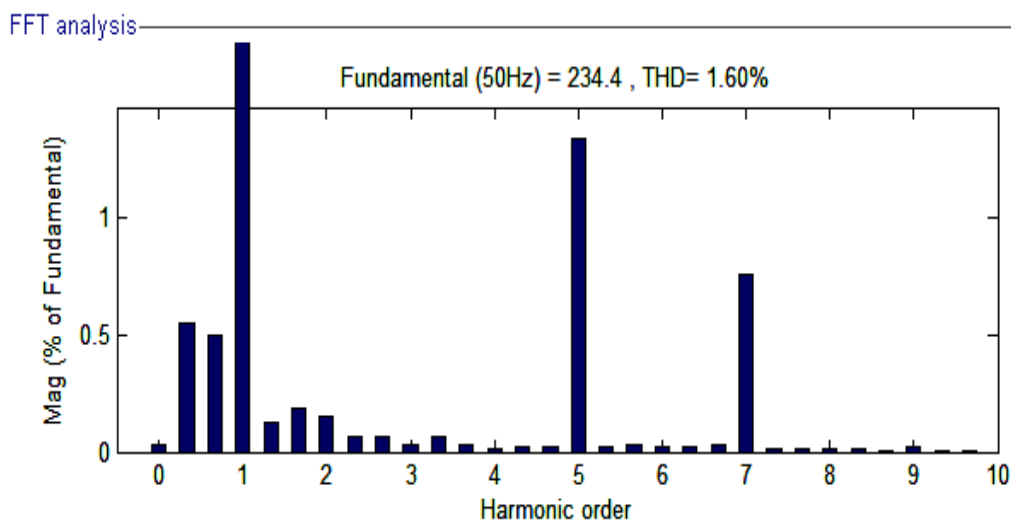


Figure 11: Total harmonic distortion

## 5. CONCLUSIONS

The proposed work concludes that the system is operating in a stable state and the voltage fluctuations from the PV source are overcome with the help of an embedded SL Quasi Z Source inverter with a dual power source. The proposed PI controller achieves high voltage gain during the shoot-through intervals and has low capacitor stress with less inrush current in the PV system. Since the basic SPWM is implemented, the complexity of the system is reduced and has robustness for the proposed topology. A fluctuation input of voltage 100 volts – 150 volts is applied to MPPT and the DPS of 30 volts each is injected. A constant output voltage of 234 volts is obtained without any fluctuations. Apart from this, the THD is much reduced to 1.64% and the system yields better efficiency. The proposed topology is gently suitable for induction motor drives.

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